## Q10021212 - We do not get any LED drive activity when using an Areca ARC-1882ix-4G Version 3, Intel 520 Series Solid State Drives Model # SSDSC2CW480A310 and Chenbro chassis model. The Chenbro chassis model with the identified integrated Drive Backplanes and LED Board Module parts are: Chenbro Chassis Model # RM41416M2 Chenbro Drive Backplanes Model # 80H10321516A1 F:VI Chenbro LED Board Module Model # 80H033215-005 Rev:A3

When using this specific Chenbro Drive Backplane model & LED Board Module model with specific Intel 520 Series Solid State Drives Model # SSDSC2CW480A310, it is necessary to modify the onboard switch setting on the Chenbro LED Board Module in order to identify drive activity operation on the drive LEDs.

The Chenbro LED Board Module Model # 80H033215-005 Rev:A3 has an onboard 6 position switch with designator location "SW1".

Chenbro has identified that when using the Intel 520 Series of solid state drives with these specific drive backplanes and LED board models, the function of the drive activity LEDs are affected by the "sequential spin-up" setting on the LED board module.

On the LED Board Module, dip position #4 on the switch is used for the drive "sequential spin-up" setting.

When the "sequential spin-up" setting is set to "OFF", the drive activity LEDs will then also be OFF at all times when using Intel 520 Series solid state drives.

When the "sequential spin-up" setting is set to "ON", the drive activity LEDs will then stay ON when drives are in idle mode and will flash when drives are in access operations when using the Intel 520 Series solid state drives.

Please note that this Chenbro chassis model, drive backplanes, and LED board module models do not have this issue with all drive models; but this issue has been confirmed for the Intel 520 Series solid state drive models.

Other Chenbro chassis models with different drive backplane models do not exhibit this issue with Intel 520 Series solid state drive modes.

Unique solution ID: #1363 Author: Simone Last update: 2022-01-06 08:31

> Page 2 / 2 (c) 2025 Vincent C. <vincent\_chen@abacid.com> | 2025-07-01 09:52 URL: http://faq.areca.com.tw//index.php?action=artikel&cat=0&id=364&artlang=en